**Report of Nine Benchmarks in MiBench Suite**

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| Benchmark Name | Cache Occupancy for Level-1 Data Cache (%) | Cache Occupancy for Level-1 Instruction Cache (%) | Miss Rate for Level-1 Data Cache | Miss Rate for Level-1 Instruction Cache |
| basicmath | 0.069317 | 0.484173 | 0.000003 | 0.000018 |
| bitcount | 0.080477 | 0.528225 | 0.000110 | 0.000429 |
| CRC32 | 0.175465 | 0.058465 | 0.000035 | 0.000003 |
| dijkstra | 0.860209 | 0.862271 | 0.001995 | 0.000349 |
| FFT | 0.635835 | 0.652761 | 0.000091 | 0.000031 |
| patricia | 0.987609 | 0.909262 | 0.002135 | 0.115211 |
| qsort | 0.976217 | 0.689045 | 0.000478 | 0.000038 |
| sha | 0.306238 | 0.652004 | 0.000038 | 0.000058 |
| susan | 0.559061 | 0.024594 | 0.000052 | 0.000002 |

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| Benchmark Name | Miss Rate for Level-2 Cache | Branch Target Buffer Hit Rate (%) | Instruction Per Cycle (IPC) |
| basicmath | 0.981818 | 98.673403 | 1.791908 |
| bitcount | 0.957286 | 51.826504 | 1.600047 |
| CRC32 | 0.119490 | 39.992700 | 2.086998 |
| dijkstra | 0.062726 | 99.891064 | 1.504050 |
| FFT | 0.782401 | 72.320259 | 1.334647 |
| patricia | 0.012782 | 89.593522 | 0.975233 |
| qsort | 0.983102 | 99.972365 | 1.977259 |
| sha | 0.983287 | 99.701301 | 2.027326 |
| susan | 0.999070 | 99.229096 | 2.071750 |

**Simulated Processor** = An out-of-order X86 CPU